

# Abstracts

## Microstrip FED Planar Frequency-Multiplying Space Combiner

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*S. Nam, T. Uwano and T. Itoh. "Microstrip FED Planar Frequency-Multiplying Space Combiner." 1987 MTT-S International Microwave Symposium Digest 87.2 (1987 Vol. II [MWSYM]): 945-948.*

A frequency-multiplying power combining array has been made of slots in the ground plane on a substrate with a microstrip feed line on the backside. The second harmonic generated by a diode in each slot is combined in free space. A design procedure and experimental results are presented.

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